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## **Q&A**

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**Exam** : **4A0-M01**

**Title** : Alcatel-Lucent IP/MPLS  
Mobile Backhaul Transport

**Version** : Demo

1. When researching carrier-class Ethernet standards to support the mobile backhaul Ethernet transport, which standards body would you consult?

- A. IEEE
- B. MEF
- C. ITU-T
- D. NGMN

**Answer: B**

2. The IETF is known for which standardization effort?

- A. It develops recommendations for core network functionality, broadband service delivery and next generation services
- B. It maintains Requests for Comment (RFC) that describe technical solutions to Internet challenges
- C. It creates single, integrated network design guidelines to support mobile broadband services
- D. It develops radio access services and systems for high capacity mobile networks

**Answer: B**

3. Click on the exhibit.

```
A:MLS2# show port 1/2/2.3.5.1.1

-----
TDM DS0 Chan Group
-----
Description      : DS0GRP
Interface        : 1/2/2.3.5.1.1
TimeSlots       : 1-24
Speed           : 64
Admin Status    : up
BER SF Link Down : disabled
Last State Change : 11/15/2011 13:37:42
CRC              : 32
Oper Status     : down
Chan-Grp IfIndex : 574687879

Configured mode : access
Admin MTU       : 1524
Scramble        : true
Physical Link   : no
Idle Cycle Flags : n/a
Payload Fill Type : n/a
Signal Fill Type : n/a
Ing. Pool % Rate : 100
Egr. Sched. Pol : N/A

Encap Type      : atm
Oper MTU       : 1524
Bundle Number   : none
Load-balance-algo : Default
Payload Pattern : N/A
Signal Pattern  : N/A
Egr. Pool % Rate : 100

-----
...output truncated
```

Given the following:

- On the OC-3 port, each provisioned channel group contains all available timeslots.

The command result illustrates which circuit status?

- A. The E1 channel group is administratively turned down
- B. The DS1 circuit physical link is operationally down
- C. The IMA bundle has no operational member links
- D. The associated Layer 3 interface is operationally down

**Answer: B**

4. In the Alcatel-Lucent SROS, which command creates the OC-3 path's Administrative Unit (AU)-4 Virtual Container (VC) capacity?

- A. configure port 3/1/2 sonet-sdh path au-4

- B. configure port 3/1/2 sonet-sdh path sts3
- C. configure port 3/1/2 sonet-sdh path tug-3
- D. configure port 3/1/2 sonet-sdh path vc-4

**Answer: B**

5. Click on the exhibit.

```

A:MLS1# show port 1/2/1.1.2.2.1
=====
TDM DS0 Char Group
=====
Description      : DS0GRP
Interface       : 1/2/1.1.2.2.1
TimeSlots      : 2-32
Speed          : 64
Admin Status    : up
BER SF Link Down : disabled
Last State Change : 11/15/2011 12:41:41
CRC            : 16
Oper Status    : up
Chan-Grp IfIndex : 574652640

Configured node : access
Admin MTU      : 1502
Scramble       : false
Physical Link  : yes
Idle Cycle Flags : flags
Payload Fill Type : n/a
Signal Fill Type : n/a
Ing. Pool & Rate : 100
Egr. Sched. Pol : N/A

Encap Type     : ipcp
Oper MTU      : 1502
Bundle Number  : none
Load-balance-algo : Default
Payload Pattern : N/A
Signal Pattern  : N/A
Egr. Pool & Rate : 100
=====
... output truncated

```

On the OC-3 port, each provisioned channel group contains all available timeslots  
 The command result illustrates which OC-3 port characteristic?

- A. On STS1-1, the second VT1.5 in the second VTG is set for IPCP encapsulation
- B. On STS1-1, the second DS1 channel group in the second VTG is operationally up
- C. On STS1-1, the second E1 channel group in the second TUG-2 is operationally up
- D. On STS1-1, the second VT2 in the second TUG-2 is set for IPCP encapsulation

**Answer: C**

6. Which statement correctly describes the Point-to-Point (PPP) Multilink Protocol (MP) negotiation process?

- A. At least one link must complete Link Control Protocol (LCP) negotiations before the bundle can initialize
- B. The endpoints indicate their desire to implement MP in the link Network Control Protocol (NCP) phase
- C. The bundle must complete LCP negotiations before it can enter the NCP negotiation phase
- D. Internet Protocol-Control Protocol (IPCP) negotiations must succeed for the bundle links to initialize

**Answer: A**

7. In the Alcatel-Lucent SROS, which payload type set in a SDH-framed OC-12 port creates individual E1 containers?

- A. Virtual Tributary (VT) 1.5
- B. VT2
- C. Virtual Container (VC)-11

D. VC-12

**Answer: B**

8. Click on the exhibit.

```
A:MLS2# show multilink-bundle

=====
Bundle Summary
=====
Bundle          Type    Admin   Oper   Port   Min   Total/
Id              State   State   State  State  Links Active Links
-----
bundle-ima-1/2.5  ima-grp Up       Down   Link Up  2     4/2
=====
Bundles : 1
=====
```

The command result indicates which condition on the Inverse Multiplexing over ATM (IMA) bundle?

- A. The number of active links has dropped below the minimum threshold
- B. The associated Layer 3 interface is administratively down
- C. The remote IMA bundle is administratively down
- D. The parent OC-3 is operationally down

**Answer: C**

9. Which timing technique can deliver time of day and phase synchronization information to the base station without concern for Packet Delay Variation (PDV)?

- A. Building Integrated Timing Supply (BITS)
- B. Global Positioning System (GPS)
- C. Adaptive Clock Recovery (ACR)
- D. IEEE 1588v2/Precision Time Protocol (PTP)

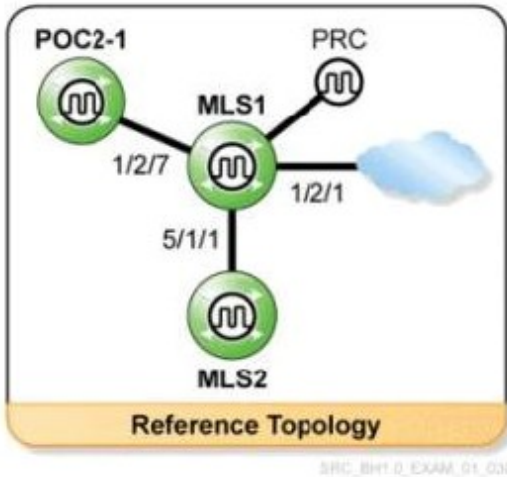
**Answer: B**

10. Which messages, periodically sent between an IEEE 1588v2/Precision Time Protocol (PTP) v2 master and slave, serve as hellos to help the slave choose the best available master?

- A. Announce
- B. Announce\_grant
- C. Sync
- D. Delay\_response

**Answer: A**

11. Click on the exhibit.



```

ref-order ref1 bits ref2
ref1
    source-port 1/2/1
    no shutdown
exit
ref2
    source-port 5/1/1
    no shutdown
    ql-override prs
exit
bits
    interface-type ds1 sf
    ql-override prs
input
    no shutdown
exit
exit
revert
    
```

Consider the topology and MLS1 configuration shown, and given the following conditions:

- MLS1 delivers the PRC traceable clock to the network
- Reference 1 receives Quality Level (QL) - ST3
- Reference 2 receives QL - DUS
- BITS sets QL-STU

Which quality level will MLS1 deliver to POC2-1 on its Synchronous Ethernet (SyncE) port 1/2/7?

- A. QL-EEC2
- B. QL-PRS
- C. QL-ST3
- D. QL-STU

**Answer: C**

12. Click on the exhibit.

```

A:CSA2# show system ptp clock 1 ptp-port 1 peer 1
-----
Peer-1
-----
IP Address       : 192.0.2.0          static/dynamic   : static
Current Master   : TRUE
Description      : MLS1
Clock Id         : 0ca402fffe96b401 Port Number       : 1
GM Clock Id      : 0ca402fffe96b401 GM Clock Class    : 80
GM Clock Accuracy : unknown(254)   GM Clock Variance : not computed
GM Clock Priority1 : 128             GM Clock Priority2 : 128
Step Type        : one-step
Last Rx Anno Msg : 11/22/2011 10:28:05
-----
Unicast Info
-----
Dir Type   Rate   Dur Result   Time              Remain
-----
Rx  Anno    1 pkt/2 s 300 granted  11/22/2011 10:25:56 172
   Sync    64 pkts/s 300 granted  11/22/2011 10:26:02 178
   Delayresp 64 pkts/s 300 granted  11/22/2011 10:26:02 178
-----
... output truncated
    
```

Given the show command results shown:

With what IEEE 1588v2/Precision Time Protocol (PTP) v2 message did the slave node set the Unicast

message rates used by the master?

- A. Announce granted
- B. Announce request
- C. Sync granted
- D. Sync request

**Answer: B**

13.Which statement best describes adaptive timing techniques?

- A. Adaptive timing calculates the time of day from time stamped packets
- B. Adaptive timing calculates the time of day from the arriving packet rate
- C. Adaptive timing only supports frequency synchronization
- D. Adaptive timing only operates on point-to-point links

**Answer: A**

14.If an IEEE 1588v2/Precision Time Protocol (PTP) v2 slave times out announce messages with the master, to which state does the slave port transition to choose another potential master?

- A. Initial
- B. Listening
- C. Passive
- D. Un-calibrated

**Answer: B**

15.Click on the exhibit.

```
-----  
    ref-order external ref1 ref2  
    ref1  
        source-ptp-clock 1  
        no shutdown  
    exit  
    ref2  
        source-port 1/2/7  
        no shutdown  
        ql-override prc  
    exit  
    external  
        input-interface  
            impedance high-impedance  
            no shutdown  
        exit  
    exit  
-----
```

Given the configuration shown and the following conditions:

- \* The external reference is offline
- \* Reference 1 receives Quality Level (QL) - EEC1

\* Reference 2 receives QL - SSU-A

Which quality level will the SAR router advertise to its Synchronous Ethernet (SyncE) peers?

- A. QL-DNU
- B. QL-EEC1
- C. QL-SSU-A
- D. QL-PRC
- E. QL-UNC

**Answer: B**

16. Click on the exhibit.

```

A:CSA1# show system ptp clock 1

=====
IEEE1588 PTP Clock Information
=====
Local Clock
-----
Clock Type           : ordinary,slave   Admin State       : up
Source I/F          : ptp_source     Clock MDA         : 1/2
PTP Profile         : ituTelecomFreq  Dynamic Peers    : not allowed
Clock ID            : 48f7f1fffe0663d6  Clock Class      : 255
Clock Accuracy      : unknown(254)    Clock Variance   : not computed
Clock Priority1     : 128              Clock Priority2   : 128
Domain              : 4                Two-Step         : unknown
-----
Operational Data
-----
Parent Clock ID     : 0ca402ffffe96b401  Parent Port Number : 1
GM Clock Id        : 0ca402ffffe96b401  GM Clock Class     : 80
GM Clock Accuracy  : unknown(254)    GM Clock Variance  : not computed
GM Clock Priority1 : 128              GM Clock Priority2 : 128
-----
Slave Port Index   : 1                Slave Port State   : slave
Slave Peer Index  : 1                Slave Peer IP      : 192.0.2.0
Forward Weight     : 96              Reverse Weight     : 4
    
```

Given the show command results shown:

Which IEEE 1588v2/Precision Time Protocol (PTP) v2 parent clock value does the slave first consider when choosing its master?

- A. GM Clock Priority1
- B. GM Clock ID
- C. GM Clock Priority2
- D. GM Clock Class

**Answer: D**

17. An SROS router obtains its timing exclusively from its BITS input port. The router sets Quality Level (QL) SONET Traceability Unknown (STU) on this Superframe (SF) framed DS1 BITS reference.

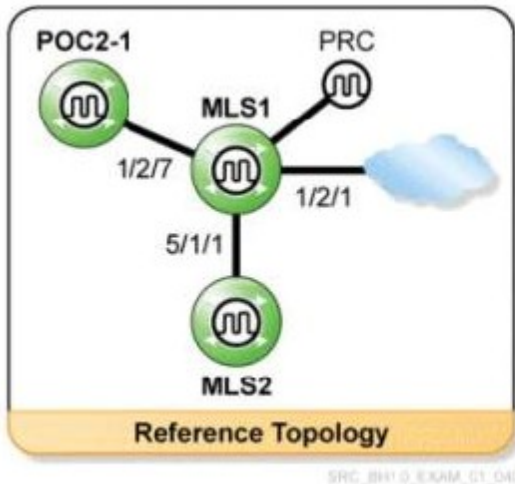
What must you configure on this master router to pass the best clock quality level to the downstream Synchronous Ethernet (SyncE) slave nodes while maintaining traceability to the DS1 source?

- A. Enable "ql-override prs" on the BITS reference
- B. Enable "ql-override prs" on the master's SyncE ports
- C. Enable "ql-selection prs" on the BITS reference
- D. Set the master router to choose its source by quality level

**Answer: A**



18. Click on the exhibit.



```

ref-order bits ref1 ref2
ref1
    source-port 1/2/1
    no shutdown
exit
ref2
    source-port 5/1/1
    no shutdown
    ql-override prc
exit
bits
    interface-type ds1 sf
    ql-override prc
input
    no shutdown
exit
exit
revert
    
```

Consider the topology and MLS1 configuration shown, and given the following conditions:

- \* MLS1 delivers the PRC traceable clock to the network
- \* Reference 1 receives Quality Level (QL) - EEC1
- \* Reference 2 receives QL - SSU-A
- \* BITS sets QL-SSU-B

Which quality level will MLS1 deliver to POC2-1 on its Synchronous Ethernet (SyncE) port 1/2/7?

- A. QL-EEC1
- B. QL-PRC
- C. QL-SSU-A
- D. QL-STU

**Answer: B**

19. Which synchronization technique supports time of day and phase synchronization?

- A. IEEE 1588 v2/Precision Time Protocol (PTP)v2
- B. Adaptive Clock Recovery (ACR)
- C. Time Division Multiplexing (TDM) line timing
- D. Synchronization Ethernet (SyncE)/Synchronization Status Message (SSM)

**Answer: A**

20. Which is a characteristic of the IEEE 802.3 Slow Protocol?

- A. There are a maximum of 10 frames transmitted per second
- B. There are a maximum of 20 slow protocol subtypes per interface
- C. The maximum slow protocol frame size is 64 bytes
- D. The slow protocol header carries the clock quality level

**Answer: A**